



XA-9625
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasutoshi AIBARA et al.

Appln. No.: 10/080,586 Group Art Unit: 2878

Filed: February 25, 2002 Examiner: S. Allen

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
IMAGING SYSTEM

Allowed: December 2, 2003

Confirmation No.: 8708

* * *

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Box Issue Fee

Sir:

Supplementing the Examiner's Statement of Reasons for Allowance, Applicants wish to state for the record that, while the Examiner's Statement of Reasons for Allowance clearly tracks the recitations of independent Claim 1, there are some differences in the recitations of independent Claims 5 and 10. Nevertheless, Claims 5 and 10

are clearly allowable for the reasons stated on pages 12 and 13 of the Amendment dated November 10, 2003.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been requested separately, such extension is hereby requested.

Respectfully submitted,

By: Nelson H. Shapiro
Nelson H. Shapiro
Reg. No. 17,095

NHS:lat

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February 27, 2004